FIG. 1

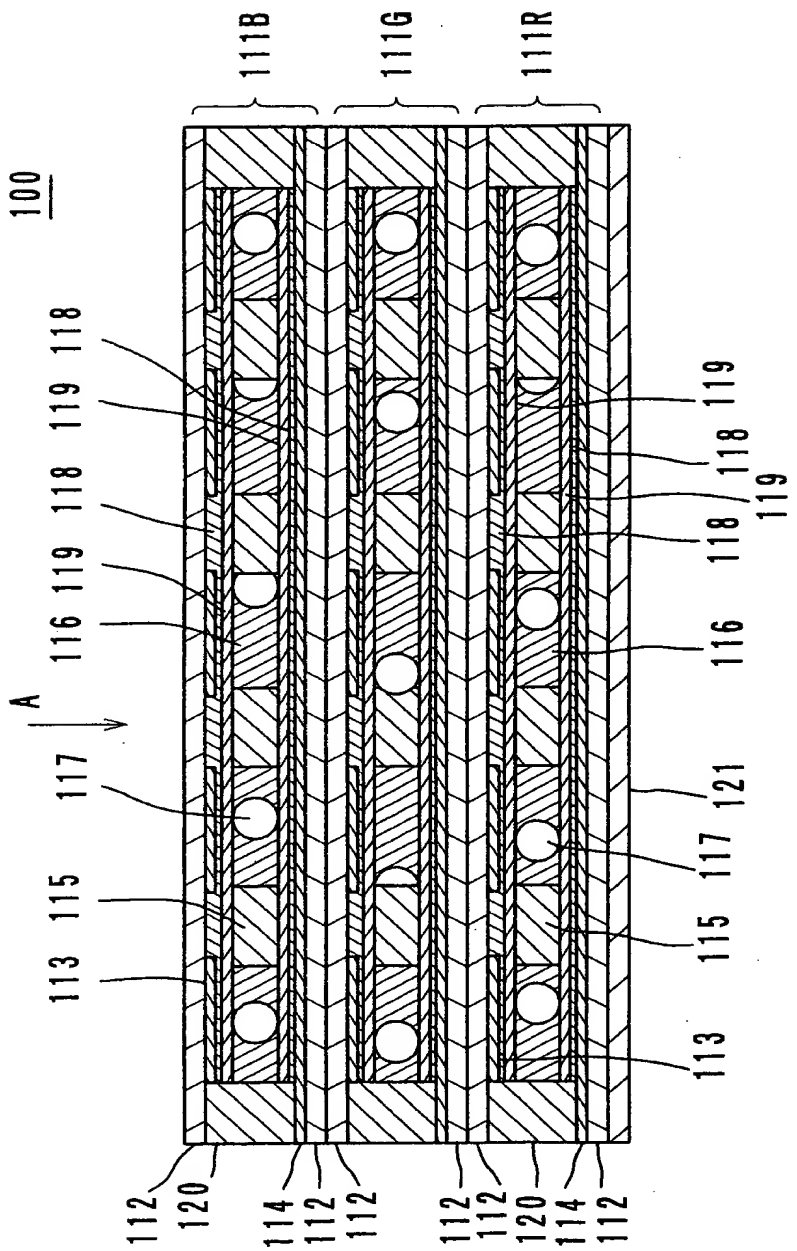

FIG. 2

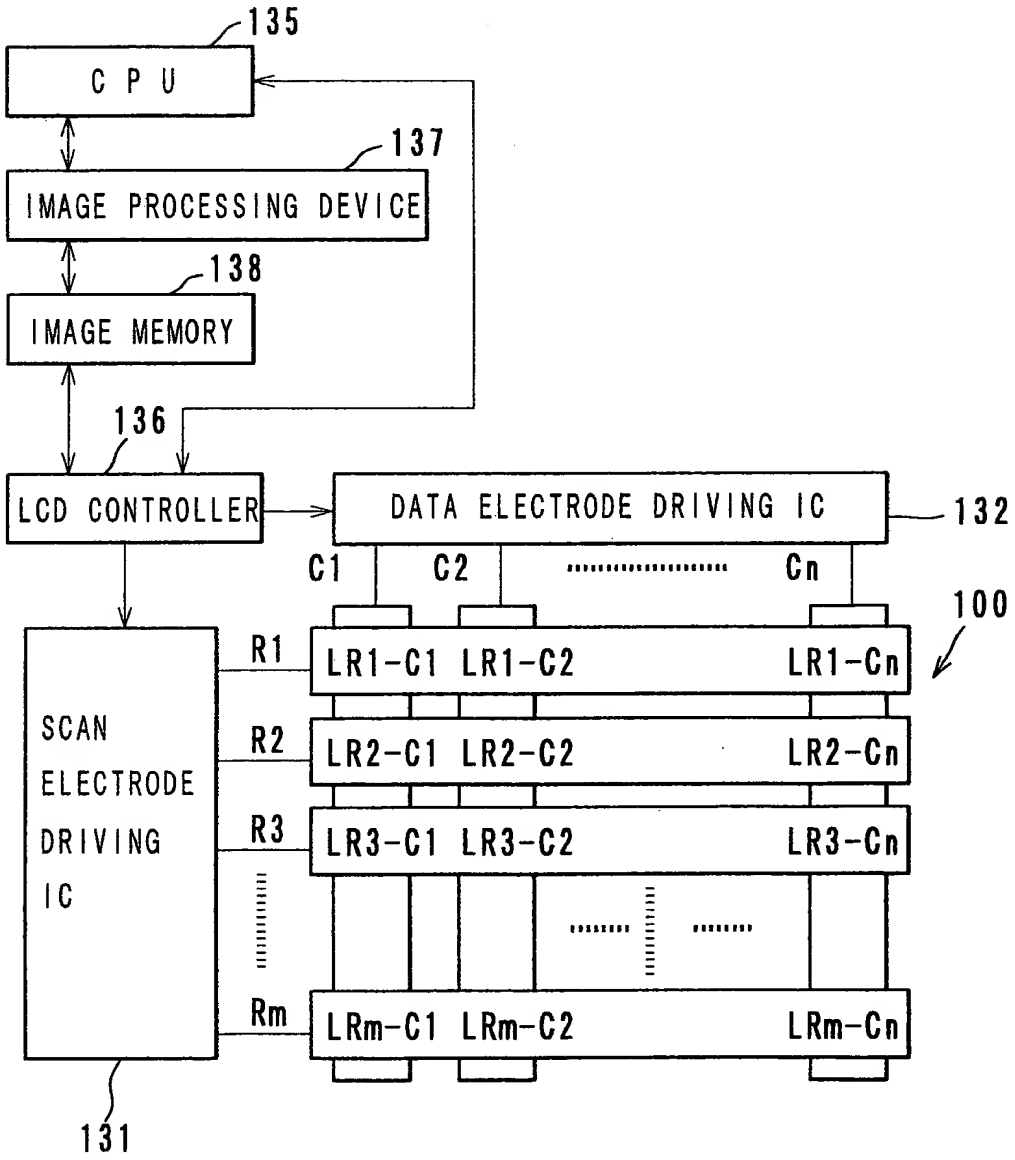
135
C P U
137
IMAGE PROCESSING DEVICE
138
IMAGE MEMORY
136
LCD CONTROLLER
DATA ELECTRODE DRIVING IC
132
C1
C2
Cn
100
SCAN ELECTRODE DRIVING IC
R1
R2
R3
Rm
LR1-C1 LR1-C2 LR1-Cn
LR2-C1 LR2-C2 LR2-Cn
LR3-C1 LR3-C2 LR3-Cn
LRm-C1 LRm-C2 LRm-Cn
131

FIG. 3

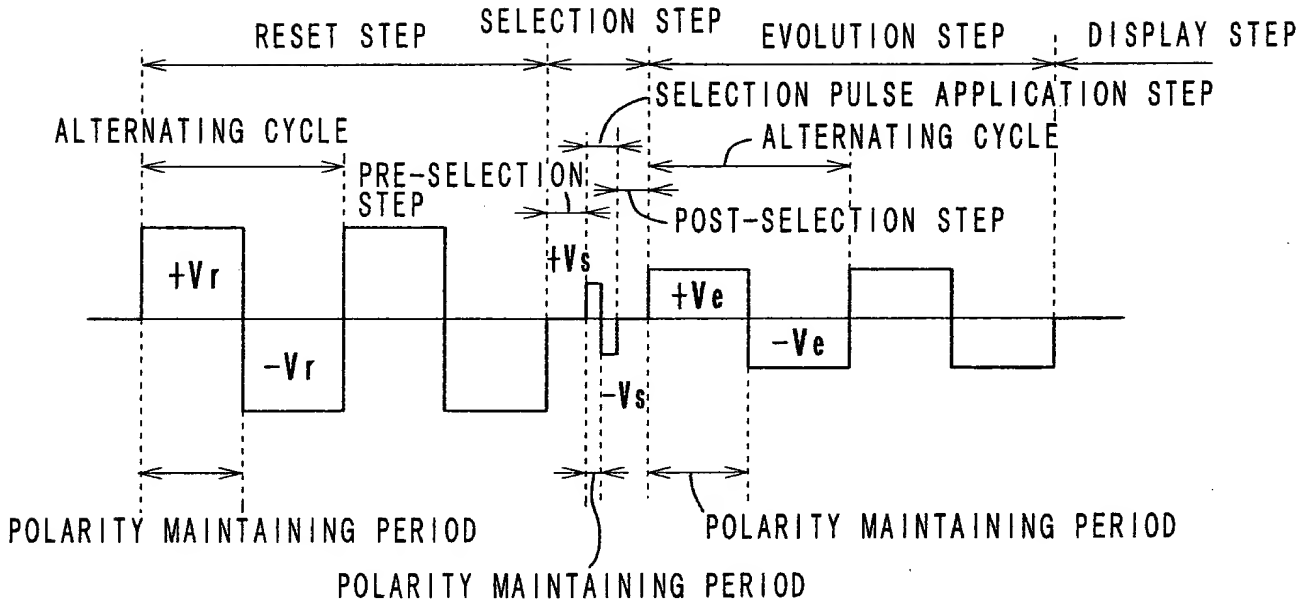
RESET STEP
SELECTION STEP
EVOLUTION STEP
DISPLAY STEP
SELECTION PULSE APPLICATION STEP
ALTERNATING CYCLE
ALTERNATING CYCLE
PRE-SELECTION STEP
POST-SELECTION STEP
+Vr
-Vr
+Vs
-Vs
+Ve
-Ve
POLARITY MAINTAINING PERIOD
POLARITY MAINTAINING PERIOD
POLARITY MAINTAINING PERIOD

FIG. 4

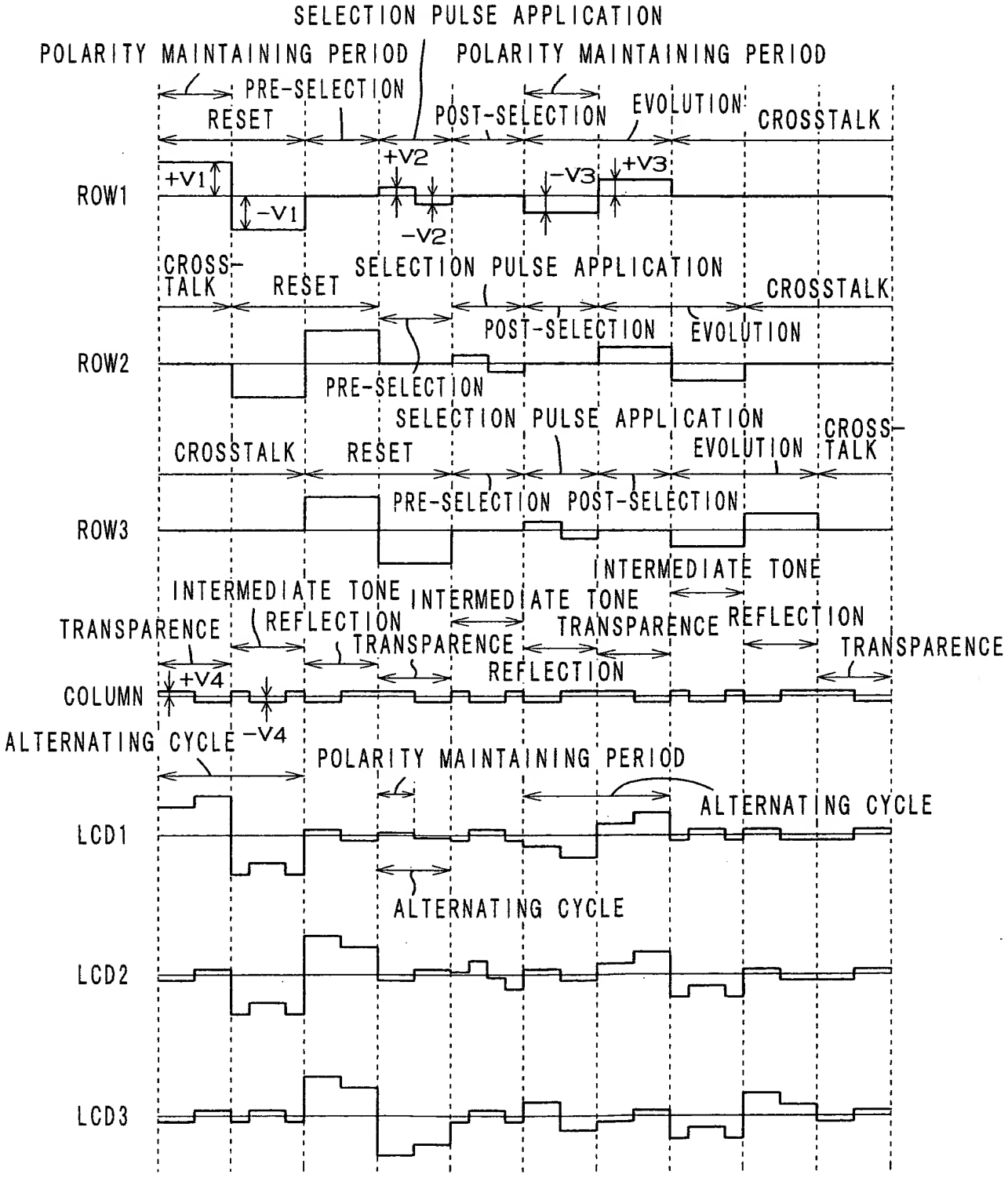
SELECTION PULSE APPLICATION
POLARITY MAINTAINING PERIOD
POLARITY MAINTAINING PERIOD
PRE-SELECTION
RESET
POST-SELECTION
EVOLUTION
CROSSTALK
+V1
-V1
+V2
-V2
-V3
+V3
ROW1
CROSS-TALK
RESET
SELECTION PULSE APPLICATION
CROSSTALK
POST-SELECTION
EVOLUTION
ROW2
PRE-SELECTION
SELECTION PULSE APPLICATION
CROSSTALK
RESET
EVOLUTION
CROSS-TALK
PRE-SELECTION
POST-SELECTION
ROW3
INTERMEDIATE TONE
INTERMEDIATE TONE
INTERMEDIATE TONE
TRANSPARENCE
REFLECTION
TRANSPARENCE
TRANSPARENCE
REFLECTION
TRANSPARENCE
REFLECTION
+V4
COLUMN
ALTERNATING CYCLE
-V4
POLARITY MAINTAINING PERIOD
ALTERNATING CYCLE
LCD1
ALTERNATING CYCLE
LCD2
LCD3

FIG. 5

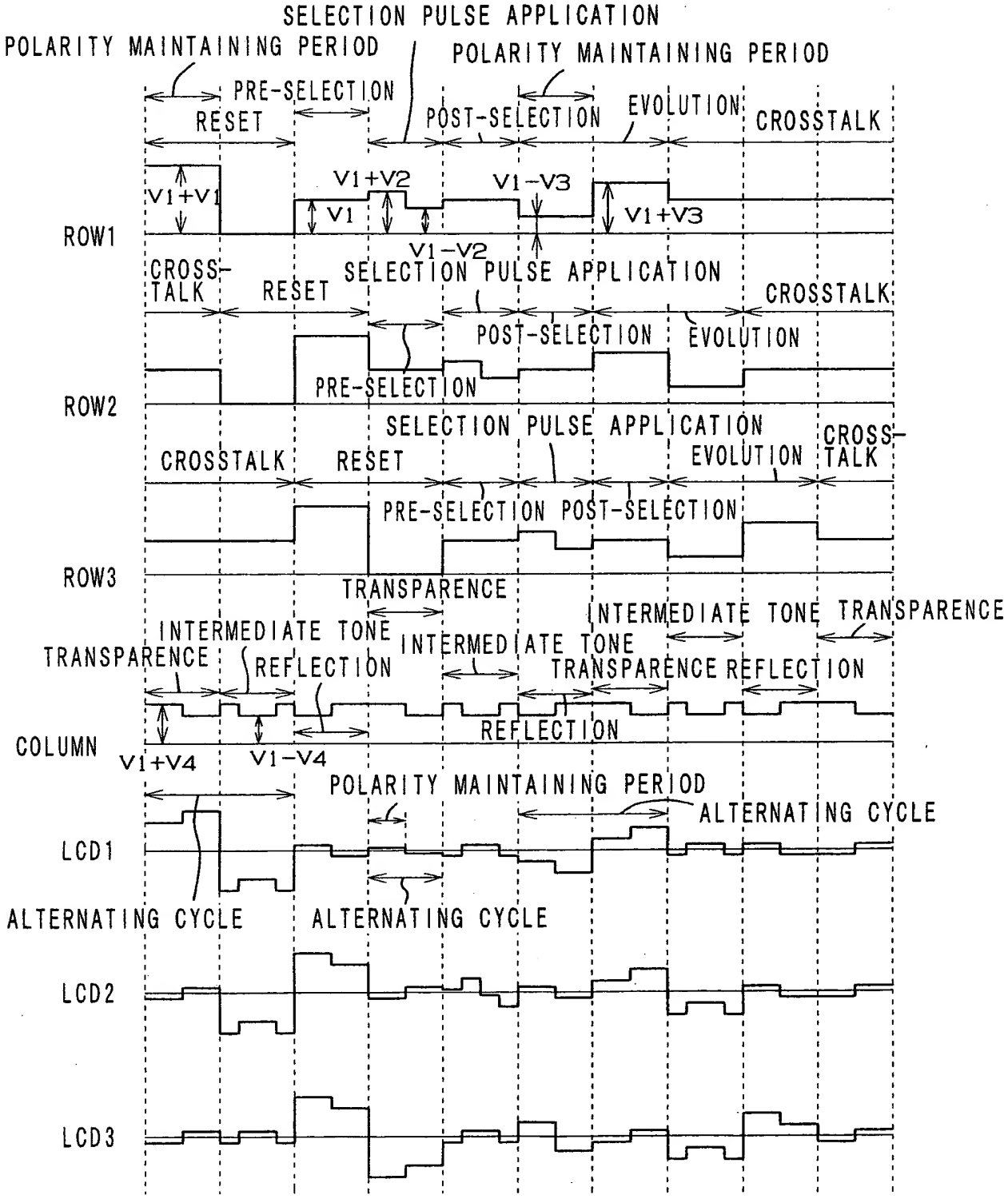
SELECTION PULSE APPLICATION
POLARITY MAINTAINING PERIOD
POLARITY MAINTAINING PERIOD
PRE-SELECTION
RESET
POST-SELECTION
EVOLUTION
CROSSTALK
V1+V1
V1+V2
V1
V1-V2
V1-V3
V1+V3
ROW1
CROSS-TALK
RESET
SELECTION PULSE APPLICATION
CROSSTALK
POST-SELECTION
EVOLUTION
PRE-SELECTION
ROW2
SELECTION PULSE APPLICATION
CROSS-TALK
CROSSTALK
RESET
EVOLUTION
PRE-SELECTION POST-SELECTION
ROW3
TRANSPARENCE
INTERMEDIATE TONE TRANSPARENCE
INTERMEDIATE TONE
INTERMEDIATE TONE
TRANSPARENCE
REFLECTION
TRANSPARENCE REFLECTION
REFLECTION
COLUMN
V1+V4
V1-V4
POLARITY MAINTAINING PERIOD
ALTERNATING CYCLE
LCD1
ALTERNATING CYCLE
ALTERNATING CYCLE
LCD2
LCD3

FIG. 6

PRIOR ART